Control scheme based on capacitor voltage second-order complex-vector feedforward for LCL-filtered inverter

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Abstract. For an inverter with LCL filter, voltage at the point of common coupling (PCC) is equivalent to the filter capacitor voltage in practical applications. However, the PCC voltage background harmonics will cause the distortion of grid current. Some problems exist in the traditional methods for harmonics suppression, such as insufficient phase margin, increasing design complexity and computation burden. Thus, a scheme of second-order complex-vector feedforward (SOCVF) of capacitor voltage is proposed in this article. First, based on the vectorial principle, the active unit vectors of capacitor voltage were calculated. Then, its fundamental component was obtained by the SOCVF-function. Furthermore, the references of the grid current were obtained with the instantaneous power theory. Hence, the proposed scheme helps effectively dampen the LCL resonance to make the system stable, and works well at suppressing the injected grid current distortion simultaneously. Simulation and experimental results validated the effectiveness of the proposed scheme.

1 Introduction

The high penetration of distributed power generation systems (DPGS) work well in alleviating the problems of environmental pollution and energy shortage. More and more grid-connected inverters are installed as the interface between the DPGS and the grid, and greatly improve the quality of injected grid current [1-2]. In the applications, the LCL filter is generally adopted due to its better ability in harmonics attenuation and smaller size compared with the L filter [3]. However, a resonance peak exists in the LCL filter, which makes the system unstable [4]. Thus, the method called filter capacitor current proportional feedback (FCCPF) has been used widely due to its relatively simple structure [5]. Nevertheless, the additional sensors for sensing the capacitor current are needed, which increases the cost of the system.

As the grid impedance exists in practical applications, what's more, the inverter is usually connected to the PCC by a step-up transformer. So that the grid voltage is difficult to directly measure, and the PCC voltage is often used instead [6-7]. Meanwhile, since the voltage of LCL-grid-side inductor is much smaller than the PCC voltage, it can be

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considered that the PCC voltage is equivalent to the voltage of filter capacitor [8]. However, with more and more non-linear devices connect to the PCC, the injected grid current distorts because of the increasing of the background harmonics [9]. To suppress the serious distortion of the grid voltage, the total harmonic distortion (THD) of injected grid current must not exceed 5% according to the regulation IEC 61000-3-4. Thus, the harmonics attenuation ability should be considered sufficiently of the controller design.

For suppressing the current distortion, extensive studies have been conducted. In [10], based on the traditional proportional-resonant (PR) control, a structure of multiple paralleled resonators is proposed to suppress the multiple harmonics of the injected grid current. However, it increases the computation burden and design complexity of the control system. In contrast, a structure based on the combination of proportional integral (PI) controller and the repetitive controller (RC) is proposed in [11], which has smaller computation burden and a simpler structure. The two methods above are both to boost the loop gain of current of the fundamental and background harmonic frequencies. However, it might make system instable due to the insufficiency of phase margin [12]. To address this issue, the scheme called grid-voltage feedforward (GVF) is adopted [13], which can suppress the multiple current harmonics simultaneously and do not effects the loop gain of current. However, for an LCL-filtered inverter, the GVF actually realizes by the capacitor voltage of LCL according to previous analysis, which still affects the stability of the system.

This article proposes a scheme of second-order complex-vector feedforward (SOCVF) of capacitor voltage. It can not only suppress the distortion of the injected grid current, but also dampen the LCL resonance. As a result, the system becomes stable, and has superior harmonics attenuation ability. Finally, the performance of the proposed scheme is verified by the results of digital simulation and experiment.

2 Conventional active damping scheme of LCL-filtered inverter

The three-phase four-wire LCL-filtered grid-connected inverter topology is shown in Fig. 1. The structure of the converter bridge is neutral point clamped (NPC). The filter is LCL-type, which consists of the NPC-side inductor L_1 , the filter capacitor C_f and the grid-side inductor L_2 . C_{dc1} , C_{dc2} are split DC capacitors, and point-N is linked to the neutral-point of the clamping diode D_1 , D_2 [14]. u_g is the grid voltage, and L_g is the grid impedance. $u_x(x=a,b,c)$ is the output voltage of the converter bridge. u_{cf} is the voltage of C_f , and i_{cf} is the current flowing through L_1 , and i_2 is the current injecting to the grid.



Fig. 1. Main Circuit of three-phase four-wire LCL-filtered grid-connected inverter.

Assuming that all components are ideal, and the L_g is neglected. According to Fig. 1, the *s*-domain state equation of the LCL-filtered grid-connected inverter can be obtained as:

$$\begin{bmatrix} u_{x}(s) \\ u_{cf}(s) \\ i_{1}(s) \end{bmatrix} = \begin{bmatrix} u_{cf}(s) \\ u_{g}(s) \\ i_{2}(s) \end{bmatrix} + s \begin{bmatrix} L_{1} & 0 & 0 \\ 0 & L_{2} & 0 \\ 0 & 0 & C_{f} \end{bmatrix} \begin{bmatrix} i_{1}(s) \\ i_{2}(s) \\ u_{cf}(s) \end{bmatrix}$$
(1)

As seen, it is a 3rd-order system, and exists LCL resonance peak that may lead to the oscillation of both i_1 and i_2 . Conventionally, the active damping method of FCCPF is used to dampen the resonance peak.

The control block diagram of FCCPF is given in Fig. 2. Where, i_x^* ($x=\alpha,\beta$) is the reference of grid current i_{2x} , H_{i2} is the sensor gain of i_{2x} , and H_{ic} is the coefficient of FCCPF. $G_i(s)$ is the current regulator with quasi-proportional-resonant (qPR) structure. $Z_{L1}=sL_1$, $Z_{L2}=sL_2$, $Z_C=1/sC_f$ are the impedance of L_1 , L_2 , and C_f respectively. $K_{PWM}=U_{dc}/U_{tri}$ is the transfer function of NPC converter bridge, where U_{dc} is the DC voltage, and U_{tri} is the amplitude of the triangular carrier. $e^{-1.5sT_a}$ is the 1.5-sampling-period delay that consists of calculation and PWM delay [15].



Fig. 2. Control block diagram of FCCPF active damping.

By applying the superposition theorem, the expression of $i_{2x}(s)$ can be obtained from the structure relationship in Fig. 2 as:

$$i_{2x}(s) = \frac{G_{o}(s)}{H_{i2} + H_{i2}G_{o}(s)}i_{x}^{*}(s) - \frac{G(s)}{1 + G_{o}(s)}u_{gx}(s) = i_{2x1}(s) + i_{2x2}(s)$$
(2)

where $G_0(s)$ is the current loop gain, $G_0(s)$ and G(s) are expressed as:

$$G_{o}(s) = \frac{H_{i2}K_{PWM}e^{-1.5sT_{s}}G_{i}(s)}{s^{3}L_{1}L_{2}C_{f} + s^{2}L_{2}C_{f}H_{ic}K_{PWM}e^{-1.5sT_{s}} + s(L_{1} + L_{2})}$$
(3)

$$G(s) = \frac{s^2 L_1 C_f + s C_f H_{ic} K_{PWM} e^{-1.5sT_s} + 1}{s^3 L_1 L_2 C_f + s^2 L_2 C_f H_{ic} K_{PWM} e^{-1.5sT_s} + s(L_1 + L_2)}$$
(4)

Although combining the qPR regulator with FCCPF can effectively dampen the LCL resonance, as seen in (2) that $i_{2x}(s)$ is composed of the static tracking component $i_{2x1}(s)$ and the disturbance component $i_{2x2}(s)$. In the ideal grid, it may only exist the grid current steady-state errors. When the grid operates in non-ideal state, the current reference i_x^* may distort, and further leads to the severe distortion of the injected grid current.

3 Capacitor voltage second-order complex-vector feedforward

To address the drawbacks of the conventional methods, this article proposes a scheme of second-order complex-vector feedforward (SOCVF) of capacitor voltage. First, the active unit vectors of capacitor voltage are calculated according to the vectorial principle. Then, its fundamental component is obtained by the SOCVF-function. Finally, the grid current

commands are obtained according to the instantaneous power theory. The detailed principles of the scheme will be described in the following sections.

3.1 Calculation of capacitor voltage unit composite vector

The instantaneous power is constant in a three-phase AC system, and the distribution of the active and reactive components indicates the actual operation state of a system. Thus, if the instantaneous voltage is transformed into active and reactive components, the active and reactive components of the grid current can be independently controlled in *dq* frame [16].

According to the analysis in Section 1, the PCC voltage u_{pcc} is equal to the filter capacitor voltage u_{cf} , thus the phase information of u_{cf} can be used as reference phase of i_x^* . Define the direction of the active voltage composite vector u_{cfs} of $u_{\text{cfx}}(x=a,b,c)$ to be consistent with the *d*-axis, lagging of 90° with respect to the reactive voltage composite vector u_{cfs} . The vector relationship is shown in Fig. 3.





Fig. 3. Active and reactive voltage vector based on $\alpha\beta$ reference frame.

Fig. 4. Realization of SOCVF-function based on unit negative feedback.

Where ωt is the phase information of u_{cfs} , and $u_{cfs\alpha}$ and $u_{cfs\beta}$ are the components of u_{cfs} in each axis of $\alpha\beta$ frame. Thus, according to Fig. 3, the unit vectors $v_{cfs\alpha}$, $v_{cfs\beta}$ of u_{cfs} , and the unit vectors $v'_{cfs\alpha}$, $v'_{cfs\beta}$ of u'_{cfs} can be derived as follows:

$$\begin{cases} \mathbf{v}_{cfs\alpha} = \frac{\mathbf{u}_{cfs\alpha}}{\sqrt{\mathbf{u}_{cfs\alpha}^2 + \mathbf{u}_{cfs\beta}^2}} = \frac{\mathbf{u}_{cfs\alpha}}{|\mathbf{u}_{cfs}|} = \cos \omega t & \begin{cases} \mathbf{v}_{cfs\alpha}' = \frac{\mathbf{u}_{cfs\alpha}'}{\sqrt{\mathbf{u}_{cfs\alpha}'^2 + \mathbf{u}_{cfs\beta}'^2}} = \frac{\mathbf{u}_{cfs\beta}}{|\mathbf{u}_{cfs}'|} = \mathbf{v}_{cfs\beta} = \sin \omega t \\ \mathbf{v}_{cfs\beta}' = \frac{\mathbf{u}_{cfs\beta}}{\sqrt{\mathbf{u}_{cfs\alpha}^2 + \mathbf{u}_{cfs\beta}^2}} = \frac{\mathbf{u}_{cfs\beta}}{|\mathbf{u}_{cfs}'|} = \sin \omega t & \end{cases} \\ \begin{cases} \mathbf{v}_{cfs\beta}' = \frac{\mathbf{u}_{cfs\beta}'}{\sqrt{\mathbf{u}_{cfs\alpha}'^2 + \mathbf{u}_{cfs\beta}'^2}} = \frac{\mathbf{u}_{cfs\beta}'}{|\mathbf{u}_{cfs}'|} = -\mathbf{v}_{cfs\alpha} = -\cos \omega t \end{cases}$$
(5)

3.2 Principle of second-order complex-vector feedforward function

As is known, the second-order scalar filter in analog electronic circuits can suppress the high-frequency harmonics, and the typical transfer function is expressed as:

$$A(s) = \frac{\omega_{\rm k}}{s^2 + 2\zeta\omega_{\rm n}s + \omega_{\rm n}^2} \tag{6}$$

where ζ is the damping ratio, which is usually 0.707 in order to optimize the dynamic performance. ω_n is the natural angular frequency, and ω_k is the adjustable gain.

However, when the negative sequence component of the input signal is relatively large, the output signal becomes distorted by adopting the typical transfer function of equation (6). Thus, for achieving the suppression of the high-frequency harmonics and extracting the fundamental component of the input signal simultaneously, a zero point of $s_z=-j\omega_0$ is

substituted into the numerator of (6). Let $\omega_n = \omega_o$, $\omega_k = \zeta \omega_o$, where ω_o is the power angular frequency. Then the proposed SOCVF-function can be expressed as:

$$A(s) = \frac{\zeta \omega_{o}(s+j\omega_{o})}{s^{2}+2\zeta \omega_{o}s+\omega_{o}^{2}} = \frac{R_{1}(s)}{1+R_{1}(s)+R_{2}(s)}$$
(7)

where $R_1(s)$ and $R_2(s)$ are expressed as:

$$R_{1}(s) = \frac{\zeta \omega_{0}}{s - j\omega_{0}}, \quad R_{2}(s) = \frac{\zeta \omega_{0}}{s + j\omega_{0}}$$
(8)

Fig. 4 shows a diagram of the realization of SOCVF-function based on unit negative feedback. Further, as shown in Fig. 5, the dashed part is the realization of SOCVF-function in scalar form, where K is the adjustable gain, and its value is 2ζ .



Fig. 5. Realization of SOCVF-function in scalar form.

The input-output relationship of Fig. 5 can be described in (9), further the amplitudefrequency characteristics of $H_d(s)$ and $H_q(s)$ can be derived as shown in (10), where ω is the input signal angular frequency. As seen in (10), when ω is taken value as ω_0 , the value of amplitude gain is 1, and the phase offset is 0. Thus the fundamental of input signal can be tracked without static error. Meanwhile, the amplitude gain will be greatly attenuated with the increment of ω , that is, the high-frequency harmonics can be effectively suppressed.

$$\begin{cases} \mathbf{v}_{\alpha\beta}(s) = \frac{2\zeta\omega_{o}s}{s^{2} + 2\zeta\omega_{o}s + \omega_{o}^{2}} \cdot \mathbf{v}_{cfs\alpha} = H_{d}(s) \cdot \mathbf{v}_{cfs\alpha} \\ q\mathbf{v}_{\alpha\beta}(s) = \frac{2\zeta\omega_{o}^{2}}{s^{2} + 2\zeta\omega_{o}s + \omega_{o}^{2}} \cdot \mathbf{v}_{cfs\beta} = H_{q}(s) \cdot \mathbf{v}_{cfs\beta} \end{cases}$$

$$\begin{cases} \left|H_{d}(j\omega)\right| = \frac{K\omega_{o}\omega}{\sqrt{(K\omega_{o}\omega)^{2} + (\omega_{o}^{2} - \omega^{2})}} = \frac{\omega}{\omega_{o}} \left|H_{q}(j\omega)\right| \\ \left|\angle H_{d}(j\omega)\right| = \arctan\left(\frac{\omega_{o}^{2} - \omega^{2}}{K\omega_{o}\omega}\right) = \angle H_{q}(j\omega) + 90^{\circ} \end{cases}$$

$$(10)$$

Furthermore, combining the principles in Section 2.1 and the instantaneous power theory, the current reference under $\alpha\beta$ -frame can be obtained as follow:

$$\begin{bmatrix} i_{\alpha}^{*} \\ i_{\alpha}^{*} \end{bmatrix} = \frac{1}{|\boldsymbol{u}_{cfs}|} \begin{bmatrix} \boldsymbol{v}_{cfs\alpha}^{+1} & \boldsymbol{v}_{cfs\alpha}^{+1} \\ \boldsymbol{v}_{cfs\alpha}^{+1} & \boldsymbol{v}_{cfs\beta}^{+1} \end{bmatrix} \begin{bmatrix} p \\ q \end{bmatrix} = \begin{bmatrix} \boldsymbol{v}_{cfs\alpha}^{+1} & \boldsymbol{v}_{cfs\alpha}^{+1} \\ \boldsymbol{v}_{cfs\alpha}^{+1} & \boldsymbol{v}_{cfs\beta}^{+1} \end{bmatrix} \begin{bmatrix} i_{\alpha}^{*} \\ i_{\alpha}^{*} \end{bmatrix} = \begin{bmatrix} \boldsymbol{v}_{cfs\alpha}^{+1} & \boldsymbol{v}_{cfs\beta}^{+1} \\ \boldsymbol{v}_{cfs\alpha}^{+1} & -\boldsymbol{v}_{cfs\alpha}^{+1} \end{bmatrix} \begin{bmatrix} i_{\alpha}^{*} \\ i_{\alpha}^{*} \end{bmatrix}$$
(11)

where i_d^* is the active current reference, and i_q^* is the reactive current reference. *p* is the instantaneous active power, and *q* is the instantaneous reactive power.

Therefore, according to the principles described above, the control block diagram of proposed scheme of SOCVF of capacitor voltage is shown in Fig. 6. Where H_v is the capacitor voltage sensor gain, and $u_{m\alpha}$, $u_{m\beta}$ are the modulation signals of NPC.



Fig. 6. Capacitor voltage SOCVF control block diagram.

4 Simulation results

In order to investigate the feasibility of the proposed scheme, a three-phase four-wire LCL grid-connected inverter system simulation model is carried out in MATLAB/Simulink, and the parameters are shown in Table 1. In addition, for testing the harmonics rejection ability of the proposed scheme, the actual measured PCC voltage data shown in Table 2 are used in simulation. As seen in Table 2, the three-phase PCC voltage in the actual grid is not only unbalanced, but also contains background harmonics. Notably, Table 2 only shows the harmonic order within 15th due to the measuring limitation of the equipment.

Parameter	Symbol	Value	Parameter	Symbol	Value
DC voltage	$U_{ m dc}$	780 V	Grid current sensor gain	H _{i2}	1
DC capacitance	C_{dc1}, C_{dc2}	7800 μF	Capacitor current feedback coefficient	$H_{\rm ic}$	0.5
PCC voltage (Line-to-line RMS voltage)	C voltage (Line-to-line $u_{\text{pcc_line}}$ 380 V RMS voltage)		Capacitor voltage sensor gain	$H_{\rm v}$	1
Fundamental frequency	fo	50 Hz	Inverter-side inductor	L_1	$400 \; \mu \mathrm{H}$
Sampling frequency	$f_{ m s}$	15.2 kHz	Filter capacitor	$C_{ m f}$	20 µF
Parameters of $G_i(s)$	$K_{\rm pi}, K_{\rm ri}$	2.5, 500	Grid-side inductor	L_2	60 µH

Table 1. Simulation model parameters.

Table 2. Fundamental and harmonics of voltage at point of common coupling.

Hari	monic order	Fundamental	3	5	7	9	11	13	15
Phase-A	RMS value (V)	233.7	5.3	5.2	2.8	2.1	1.3	1.2	0.9
	Phase angle (deg)	2	51	-146	-3	87	-94	-136	60
Phase-B	RMS value (V)	230.0	4.5	4.3	1.0	1.7	2.9	0.8	0.5
	Phase angle (deg)	-123	50	-5	-97	-256	32	-188	-278
Phase-C	RMS value (V)	263.8	5.9	6.0	1.2	2.8	1.2	0.4	1.2
	Phase angle (deg)	-238	-312	-258	-313	-263	-242	-158	-316

Setting the grid current reference value of $i_d^*=10A$, $i_a^*=0A$, Fig. 7 and Fig. 8 respectively show the simulation waveforms of steady-state grid current with the active damping scheme of FCCPF and the proposed scheme. As seen in Fig. 7, when employing the FCCPF scheme, the grid current is stable though, it is distorted obviously with a THD of 8.64%, which exceeds the industry standard of 5%. When employing the proposed scheme, as seen in Fig. 8, the distortion of i_2 is almost suppressed with the THD reducing to 2.15%. The simulation results above are consistent with the analysis in Section 3.

Furthermore, Fig. 9 shows the phase-A transient-state simulation waveforms for testing the resonant-damping performance of the proposed method. Obviously, the PCC voltage and grid current of phase-A become stable when adopting the proposed method at the moment of 0.105s, indicating the effectiveness of LCL resonance damping.



steady-state grid current by FCCPF scheme.





resonant-damping performance by proposed method.

5 Experimental results

For further verifying the proposed scheme, an experimental prototype has been fabricated. The 32-bit floating-point dual-core DSP (TI TMS320F28377D) is used for implementing the control algorithms, and the dead time of the PWM pulse signal is set to 3 μ s. A oscilloscope of TPS-2024 is used to observe the waveforms, and a power quality analyzer of Fluke-435 is used to analysis power quality of u_{pcc} and i_2 . Notably, severe LCL resonance may occur in experiment, so the following experiments are all conducted at a lower voltage class for safety. Thus, the PCC voltage in Tab. 1 is adjusted to 50V, the DC voltage is adjusted to 120V, and the others are the same as simulation. The experimental waveforms are shown in Fig. 10, Fig. 11 and Fig. 12.



Fig. 10. Experimental results of steady-state grid current by FCCPF scheme.



Fig. 12. Experimental results of resonant-damping performance by proposed method.

As seen in Fig. 10, with the active damping scheme of FCCPF, the injected grid current distorts severely with a THD of 9.1% due to the background harmonics. When employing the proposed scheme, i_2 becomes highly sinusoidal, as seen in Fig. 11. And the THD drops to 3.4%, indicating the effective harmonics rejection ability of the SOCVF scheme. Then Fig. 12 shows the transient-state experimental waveforms of phase-A for testing the

resonant-damping performance of SOCVF scheme. It is clear that the resonance of the PCC voltage and grid current are well dampened after a short time, verifying the effectiveness for damping LCL resonance of the proposed scheme. Thus, the results above are consistent with the simulation.

6 Conclusion

In this article, a control scheme of capacitor voltage second-order complex-vector feedforward is proposed to suppress the injected grid current distortion of the LCL-type grid-connected inverter caused by the background harmonics. Meanwhile, the LCL resonance can be effectively dampen. In contrast to other methods, it reduces the system complexity and computational burden. In addition, compared with the conventional method of FCCPF, the proposed scheme achieves active damping of the LCL resonance without additional capacitor current sensors, thereby significantly saving the cost in the industrial applications. Although the proposed SOCVF scheme seems more suitable for practice, further research is still needed for its stability analysis and effectiveness under more complex operating conditions.

References

- 1. C. Xie, D. Liu, K. Li, et al. Passivity-based design of repetitive controller for LCL-type grid-connected inverters suitable for microgrid applications[J]. IEEE Trans. on Power Electronics, **36**, 2420-2431 (2021)
- X. Wu, Y. Wang, X. Chen, et al. Sequence impedance model and interaction stability research of three-phase grid-connected inverters with considering coupling effects[J]. Proceedings of the CSEE, 40, 1605-1617 (2020)
- 3. E. Rodriguez-Diaz, F.D. Freijedo, J.M. Guerrero, et al. Input-admittance passivity compliance for grid-connected converters with an LCL filter[J]. IEEE Trans. on Industrial Electronics, **66**, 1089-1097 (2019)
- 4. T. Dragičević, C. Zheng, J. Rodriguez, et al. Robust quasi-predictive control of LCLfiltered grid converters[J]. IEEE Trans. on Power Electronics, **35**, 1934-1946 (2020)
- C. Bao, X. Ruan, X. Wang, et al. Design of grid-connected inverters with LCL filter based on PI regulator and capacitor current feedback active damping[J]. Proceedings of the CSEE, 32, 133-142+19 (2012)
- Q. Liu, Y. Li, S. Hu, et al. A transformer integrated filtering system for power quality improvement of industrial DC supply system[J]. IEEE Trans. on Industrial Electronics, 67, 3329-3339 (2020)
- Q. Liu, Y. Li, L. Luo, et al. Power quality management of PV power plant with transformer integrated filtering method[J]. IEEE Trans. on Power Delivery, 34, 941-949 (2019)
- 8. T. Fang, X. Zhang, C. Huang, et al. Control scheme to achieve multiple objectives and superior reliability for input-series-output-parallel LCL-type grid-connected inverter system[J]. IEEE Trans. on Industrial Electronics, **67**, 214-224 (2020)
- D. Pan, X. Ruan, X. Wang, et al. Analysis and design of current control schemes for LCL-type grid-connected inverter based on a general mathematical model[J]. IEEE Trans. on Power Electronics, 32, 4395-4410 (2017)

- C. Xie, X. Zhao, K. Li, et al. Multirate resonant controllers for grid-connected inverters with harmonic compensation function[J]. IEEE Trans. on Industrial Electronics, 66, 8981-8991 (2019)
- 11. M. Lyu, et al. Analysis and design of PI plus repetitive control for grid-side converters of direct-drive wind power systems considering the effect of hardware sampling circuits[J]. IEEE Access, **8**, 87947-87959 (2020)
- J. Xu, S. Bian, Q. Qian, et al. Grid current feedforward based phase-locked loop for sin-gle-phase-inverters in weak grid case[J]. Proceedings of the CSEE, 40, 2647-2657 (2020)
- W. Li, X. Ruan, D. Pan, et al. Full-feedforward schemes of grid voltages for a threephase LCL-type grid-connected inverter[J]. IEEE Trans. on Industrial Electronics, 60, 2237-2250 (2013)
- H. Xiao, X. Wang, X. Zhang, et al. State-of-the-art and future trend of transformerless photovoltaic grid-connected inverters[J]. Proceedings of the CSEE, 40, 1038-1054+1397 (2020)
- 15. Y. He, X. Wang, X. Ruan, et al. Hybrid active damping combining capacitor current feedback and point of common coupling voltage feedforward for LCL-type grid-connected inverter[J]. IEEE Trans. on Power Electronics, **36**, 2373-2383 (2021)
- D. Yang, N. Wu, L. Yin, et al. Natural frame control of single-phase cascaded Hbridge multilevel converter based on fictive-phases construction[J]. IEEE Trans. on Industrial Electronics, 65, 3848-3857 (2018)